

Amendments to the Specification:

Please replace the paragraph starting in line 14 of page 6 with the following amended paragraph:

Returning to FIG. 2, the figure depicts an embodiment of an evaluation circuit to be employed by the present invention. Equivalent elements and arrangements may be employed, as would be known by the skilled artisan, to accomplish the equivalent signal manipulation and output as will be set out below. Applying the arrangement depicted in FIG. 2, the output from the first and third photodetectors are feed into an adder 31 which combines the two inputs into an output. The output represents about twice the maximum output of one of the photodetectors. The output of adder 31 is fed into divider 34 which divides the signal from the adder by two. The output of the divider serves as a reference signal as it remains relatively insensitive to fluctuations in light intensity given the inverse relationship between the first and third photodetectors as depicted in FIG. 2. The reference signal is fed into a first and second comparator 32 and 33. The first comparator 32 is fed the output A from the first photodetector 21 and outputs first quadrature signal QS1. Likewise, the second comparator 33 is fed the output B from second photodetector 22 and outputs second quadrature signal QS2. The pulse rate of the two quadrature signals are indicative of the speed of the raster, while the phase of the two signals is indicative of the raster direction.